



IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

1F 2133

Patent Application

Inventor(s) Josef Schmid
Case 3
Serial No. 10/075,868
File Date February 13, 2002
Examiner John J. Tabone, Jr.
Title Boundary Scan Delay Chain For Cross-Chip
Delay Measurement

Group Art 2133

ASSISTANT COMMISSIONER FOR PATENTS AND TRADEMARK
WASHINGTON, DC 20231

Dear Sir:

Do Not Enter
JJ 8/1/05

AMENDMENT

In response to the final Office Action dated March 22, 2005, please consider the following remarks on the above-identified patent application as follows.